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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application

Applicant: Munir H. Nayfeh
Serial No.: 09/496,506
Conf. No.: 6344
Filed: February 2, 2000
For: SILICON NANOPARTICLE
FIELD EFFECT
TRANSISTOR AND
TRANSISTOR MEMORY
DEVICE
Art Unit: 2811
Examiner: Sara W. Crane

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

July 29, 2005

Date

Registration No. 43,874

F-CLASS.WCM

Appr. February 20, 1998

Attorney for Applicant

AMENDMENT AFTER NOTICE OF ALLOWANCE UNDER 37 C.F.R. 1.312

Mail Stop ISSUE FEE
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to issuance of the above-named allowed Application, please enter the following amendments: